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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10035606	FILING DATE 12/28/2001	CLASS 257	SUBCLASS 322	GAU 2811	EXAMINER <i>Hu</i>
**APPLICANTS: Gallia James; Krishnan Srikanth; Krishnan Anand;					
**CONTINUING DATA VERIFIED: <i>N/A</i>					
** FOREIGN APPLICATIONS VERIFIED: <i>N/A</i> <i>9/10/03</i>					
PG-PUB DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no Verified and Acknowledged Examiners's initials				ATTORNEY DOCKET NO TI-32942 (032350.B365)	
TITLE : Method and system for reducing charge damage in silicon-on-insulator technology U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)					

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NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
ISSUE FEE		Sheets Drwg.	Figs. Drwg.
Amount Due	Date Paid	Print Fig.	
Primary Examiner		Application Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
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